## TFT COLOR LCD MODULE NL10276AC28-02L

36 cm ( 14.1 inches), $1024 \times 768$ pixels, Full-color, Ultra-wide viewing angle, Multi-scan function, CRT interface board

## DESCRIPTION

NL10276AC28-02L is a TFT (thin film transistor) active matrix color liquid crystal display (LCD) comprising amorphous silicon TFT attached to each signal electrode, a driving circuit, a CRT interface board and a backlight. NL10276AC28-02L has a built-in backlight with an inverter.

The 36 cm ( 14.1 inches) diagonal display area contains $1024 \times 768$ pixels and can display full-color (more than 16 million colors simultaneously). Also, it has ultra wide viewing angle and multi-scan function. Therefore, we call this module Super Fine TFT.

## FEATURES

- Ultra-wide viewing angle and low reflection
- CRT interface board

Auto recognition of input signal:
Analog RGB signals, Synchronous signals (Hsync, Vsyn, Composite)
Digital control: e.g. Brightness, Display Position
Free supply voltage sequence
Corresponding to DDCI and DDC2B
Corresponding to VESA DPMS

- Multi-scan function: e.g. XGA, SXGA, XGA, SVGA, VGA, VGA-TEXT, MAC
- Incorporated edge type backlight (Four lamps into two lamp holders, Inverter)
- Lamp unit replaceable (note)

VESA : Video Electronics Standards Association
DPMS : Display Power Management Signaling
DDC1 : Display Data Channel 1
DDC2B: Display Data Channel 2B

## APPLICATIONS

- Desk-top type of PC
- Engineering workstation
- Display terminals for control system
- Monitors for process controller



## On Screen Display

Regarding the use of OSD, please note that there is possibility of conflicts with a patent in Europe and the U.S.
Thus, if such conflict might happen when you use OSD, we shall not be responsible for any trouble.
The information in this document is subject to change without notice.
Please confirm with the delivery specification before starting to design the system.

## STRUCTURE AND FUNCTIONS

A color TFT (thin film transistor) LCD module is comprised of a TFT liquid crystal panel structure, LSIs for driving the TFT array, and a backlight assembly. The TFT panel structure is created by sandwiching liquid crystal material in the narrow gap between a TFT array glass substrate and a color filter glass substrate. After the driver LSIs are connected to the panel, the backlight assembly is attached to the backside of the panel.

RGB (red, green, blue) data signals from a source system is modulated into a form suitable for active matrix addressing by the onboard signal processor and sent to the driver LSIs which in turn addresses the individual TFT cells.

Acting as an electro-optical switch, each TFT cell regulates light transmission from the backlight assembly when activated by the data source. By regulating the amount of light passing through the array of red, green, and blue dots, color images are created with clarity.

OUTLINE OF CHARACTERISTICS (at room temperature)

| Display area | $285.696(\mathrm{H}) \times 214.272(\mathrm{~V}) \mathrm{mm}$ |
| :--- | :--- |
| Drive system | a-Si TFT active matrix |
| Display colors | Full-color |
| Number of pixels | $1024(\mathrm{H}) \times 768(\mathrm{~V})$ |
| Pixel arrangement | RGB vertical stripe |
| Pixel pitch | $0.279(\mathrm{H}) \times 0.279(\mathrm{~V}) \mathrm{mm}$ |
| Module size | $330.0(\mathrm{H}) \times 255.0(\mathrm{~V}) \times 23.3(\mathrm{D}) \mathrm{mm}$ (typ.) |
| Weight | $1530 \mathrm{~g}($ typ. $)$ |
| Contrast ratio | $150: 1($ typ. $)$ |

Viewing angle (more than the contrast ratio of 10:1)

- Horizontal: $85^{\circ}$ (typ., left side, right side)
- Vertical : $85^{\circ}$ (typ., up side, down side)

Designed viewing direction

- Optimum grayscale ( $\gamma=2.2$ ): perpendicular

Color gamut 40\% (typ., At center, To NTSC)
Response time 34 ms (typ.), "black 100\%" to "white 10\%"
Luminance $\quad 200 \mathrm{~cd} / \mathrm{m}^{2}$ (typ.)
Input signals Analog RGB signals, Synchronous signals (Hsync and Vsync or Composite)
Backlight Edge light type: Four cold cathode fluorescent lamps with an inverter
Lamp holder: 141LHS11
Inverter : 150PW011
Supply voltage $\quad 12 \mathrm{~V}, 12 \mathrm{~V}$ (Logic/LCD driving, Backlight)
Power consumption 25.1 W (typ.)

## BLOCK DIAGRAM



HS : Hsync
CS : Composite synchronous signal

Note: Neither GND nor GNDB is connected to frame. These should be connected in customer equipment.

## SPECIFICATIONS

GENERAL SPECIFICATIONS

| Item | Contents | Unit |
| :--- | :---: | :---: |
| Module size | $330 \pm 0.5(\mathrm{H}) \times 255.0 \pm 0.5(\mathrm{~V}) \times 24.5(\mathrm{max}).(\mathrm{D})$ | mm |
| Display area | $285.696(\mathrm{H}) \times 214.272(\mathrm{~V})$ | mm |
| Number of dots | $1024 \times 3(\mathrm{H}) \times 768(\mathrm{~V})$ | dots |
| Pixel pitch | $0.279(\mathrm{H}) \times 0.279(\mathrm{~V})$ | mm |
| Dot pitch | $0.093(\mathrm{H}) \times 0.279(\mathrm{~V})$ | mm |
| Pixel arrangement | RGB (Red, Green, Blue) vertical stripe | - |
| Display colors | Full color | color |
| Weight | $1,600($ max. $)$ | g |

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Rating | Unit | Remarks |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | VdD | -0.3 to +14 | V | $\mathrm{Ta}_{\mathrm{a}}=25^{\circ} \mathrm{C}$ |  |
|  | VodB | -0.3 to +14 | V |  |  |
| Logic input voltage | Vin1 | -0.3 to +5.5 | V | $\begin{aligned} & \mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C} \\ & \mathrm{~V} D \mathrm{D}=12 \mathrm{~V} \end{aligned}$ |  |
| R,G,B input voltage | Vin2 | -6.0 to +6.0 | V |  |  |
| Storage temp. | Tst | -20 to +60 | ${ }^{\circ} \mathrm{C}$ | - |  |
| Operating temp. | Top | 0 to 50 | ${ }^{\circ} \mathrm{C}$ | Module surface | Note |
| Humidity (no condensation) | $\leq 95 \%$ relative humidity |  |  | $\mathrm{Ta} \leq 40^{\circ} \mathrm{C}$ |  |
|  | $\leq 85 \%$ relative humidity |  |  | $40<\mathrm{Ta} \leq 50^{\circ} \mathrm{C}$ |  |
|  | Absolute humidity shall not exceed $\mathrm{T}_{\mathrm{a}}=50^{\circ} \mathrm{C}$, $85 \%$ relative humidity) level. |  |  | $\mathrm{Ta}>50^{\circ} \mathrm{C}$ |  |

Note Measured at the LCD panel (Including self-fever)

ELECTRICAL CHARACTERISTICS
(1) Logic, LCD driving, Backlight

| Item | Symbol | Min. | Typ. | Max. | Unit | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | V ${ }_{\text {D }}$ | 11.4 | 12.0 | 12.6 | V | Logic and LCD driving |
|  | VodB | 11.4 | 12.0 | 12.6 | V | Backlight |
| Logic input "L" voltage | VIL | 0 | - | 0.8 | V | HS/CS, Vsync, SEL, UP, DOWN, SEL, RESET, EXIT, RIGHT, LEFT, DDCCLK, DDCDAT |
| Logic input "H" voltage | $\mathrm{V}_{\mathrm{H}}$ | 2.2 | - | 5.25 | V |  |
| Logic output "L" voltage | Vot | - | - | 0.4 | V | DDCDAT |
| Logic output " H " voltage | Vor | 2.4 | - | - | V |  |
| Logic input "L" current | 1 L | -1 | - | - | $\mu \mathrm{A}$ | HS/CS, Vsync |
| Logic input "H" current |  | - | - | 1 | $\mu \mathrm{A}$ |  |
| Logic output "L" current 1 | loL1 | - | - | 1 | $\mu \mathrm{A}$ | DDCDAT |
| Logic output "H" current 1 | Ioh1 | -1 | - | - | $\mu \mathrm{A}$ |  |
| Supply current | IDD | - | 630 | 900 | mA | $\mathrm{VDD}=12.0 \mathrm{~V}$ |
|  |  | - | 170 | 200 | mA | Power saving mode $\mathrm{VDD}=12.0 \mathrm{~V}$ |
|  | lodB | - | 1460 | 1600 | mA | $\begin{aligned} & \mathrm{VDDB}=12.0 \mathrm{~V} \\ & \text { (Max. luminance) } \end{aligned}$ |
|  |  | - | 0.3 | 10 | mA | Power saving mode $\mathrm{VDDB}=12.0 \mathrm{~V}$ |

## (2) Video signal (R, G, B) input

| Item | Min. | Typ. | Max. | Unit | Remarks |
| :--- | :---: | :---: | :---: | :---: | :--- |
| Maximum amplitude (white - black) | 0 <br> (black) | 0.7 <br> (white) | ${ }^{*} \mathrm{~A}$ | Vp-p | Note |
| DC input level (black) | -0.5 | - | +2.5 | V | - |
| Sync level | 0.2 | 0.3 | ${ }^{*} \mathrm{~B}$ |  |  |
| $(0.6)$ | Vp-p | G terminal <br> (sync on green) |  |  |  |
| ${ }^{*} \mathrm{~A}+{ }^{*} \mathrm{~B}$ | - | - | 1.1 | Vp-p | - |

Note Contrast adjustment is needed if the amplitude exceeds $0.7 \mathrm{Vp-p}$.

## POWER SUPPLY DESIGN

(1) Please note that the supply voltage must not be applied while the control signals (SEL, UP, DOWN, EXIT, LEFT, RIGHT and RESET) are connected to GND. Otherwise the module may cause malfunction.
(2) If the power supply voltage is applied while UP and DOWN are connected to GND, the input control signals become ineffective mode. To reset this mode, turn off the power once and turn on the power while UP and DOWN are connected to GND. Then, the mode will be released.
(3) Do not change MENUSEL setting while the module is operated.

MENUSEL selection (Hierarchical type or Toggle type) is set when the power supply voltage is applied.
(4) Inverter current wave

Inverter current wave is as follows.

maximum luminance control: $100 \%$ (Duty)
minimam luminance control: 20 \% (Duty)
Luminance control frequency $\doteqdot$ Input Vsync frequency $\times \mathrm{K}$
Input Vsync frequency $\leq 75 \mathrm{~Hz}$ : K $=4.6$
Input Vsync frequency $>75 \mathrm{~Hz}: \mathrm{K}=3.6$
(5) Ripple of supply voltage

Please note that the ripple at the input connector of the module should be within the values shown in this table. It the ripple is beyond these values, the noise may appear on the screen.

|  | VDD <br> (for logic and LCD driver) | VDDB <br> (for backlight) |
| :--- | :---: | :---: |
| Acceptable range | $\leq 100 \mathrm{mVp}-\mathrm{p}$ | $\leq 200 \mathrm{mVp}-\mathrm{p}$ |

Note The acceptable range of ripple voltage includes spike noise.

Example of the power supply connection
a) Separate the power supply
b) Put the filter

$\left[\begin{array}{c}\text { Filter }^{*} \text { (reference value) } \\ \mathrm{L}=10 \mu \mathrm{H} \text { to } 100 \mu \mathrm{H} \\ \mathrm{C}=10 \mu \mathrm{~F} \text { to } 100 \mu \mathrm{~F}\end{array}\right]$
(6) VDDB should be started up within 300 ms , otherwise, the protection circuit makes the backlight turn off.

## INTERFACE PIN CONNECTIONS

(1) Logic and LCD driving

CN1
Part No. : 1-353119-4
Adaptable socket: 1-353068-4
Supplier : Japan AMP Ltd.

| Pin No. | Symbols | Pin No. | Symbols |
| :---: | :---: | :---: | :---: |
| 1 | B | 8 | Vsync |
| 2 | GND | 9 | GND |
| 3 | G | 10 | HS/CS |
| 4 | GND | 11 | GND |
| 5 | R | 12 | DDCCLK |
| 6 | GND | 13 | N.C. |
| 7 | GND | $14 \mathbf{v}$ | DDCDAT |

Figure from socket view


Note 1: N.C. (No connection) must be open.

CN3
Part No. : DF14A-25P-1.25H
Adaptable socket: DF14-25S-1.25C
Supplier : HIROSE ELECTRIC CO., LTD.

| Pin No. | Symbols | Pin No. | Symbols |
| :---: | :---: | :---: | :---: |
| $1 \nabla$ | LEDON | 14 | EXIT |
| 2 | LEDOFF | 15 | GND |
| 3 | GND | 16 | N.C. |
| 4 | N.C. | 17 | GND |
| 5 | N.C. | 18 | GND |
| 6 | RIGHT | 19 | MENUSEL |
| 7 | LEFT | 20 | N.C. |
| 8 | RESET | 21 | N.C. |
| 9 | N.C. | 22 | N.C. |
| 10 | GND | 23 | GND |
| 11 | SEL | 24 | N.C. |
| 12 | UP | 25 | N.C. |
| 13 | DOWN |  |  |

Figure from socket view
$\qquad$
12 . . . 2425

Note 1: N.C. (No connection) must be open.

CN5
Part No. : IL-Z-8PL-SMTY
Adaptable socket: IL-Z-8S-S125C3
Supplier : Japan Aviation Electronics Industry Limited (JAE)

| Pin No. | Symbols | Pin No. | Symbols |
| :---: | :---: | :---: | :---: |
| 1 | $V_{D D}$ | 5 | GND |
| 2 | $V_{D D}$ | 6 | GND |
| 3 | $V_{D D}$ | 7 | GND |
| 4 | $V_{D D}$ | $8 \mathbf{v}$ | GND |

Figure from socket view

(2) Backlight

CN201
Part No. : IL-Z-11PL-SMTY
Adaptable socket: IL-Z-11S-S125C3
Supplier
: Japan Aviation Electronics Industry Limited (JAE)

| Pin No. | Symbols | Pin No. | Symbols |
| :---: | :---: | :---: | :---: |
| 1 | VdoB | 7 | N.C. |
| 2 | VdoB | 8 | N.C. |
| 3 | VdoB | 9 | N.C. |
| 4 | GNDB | 10 | N.C. |
| 5 | GNDB | $11 \nabla$ | N.C. |
| 6 | GNDB |  |  |

Figure from socket view
$\qquad$

Note 1: N.C. (No connection) must be open.

Rear view


PIN FUNCTION

| Symbol | I/O | Logic | Description |
| :---: | :---: | :---: | :---: |
| HS/CS | Input | Negative | Horizontal synchronous signal input or composite synchronous signal input (TTL level), Positive/Negative auto recognition |
| Vsync | Input | Negative | Vertical synchronous signal input (TTL level) <br> Positive/Negative auto recognition, Clock input for DDC1 |
| R | Input | - | Red video signal input ( $0.7 \mathrm{Vp}-\mathrm{p}$, input impedance $75 \Omega$ ) |
| G | Input | - | Green video signal input (0.7 Vp-p, input impedance $75 \Omega$ ) |
| B | Input | - | Blue video signal input ( $0.7 \mathrm{Vp}-\mathrm{p}$, input impedance $75 \Omega$ ) |
| LEDON | Output | Positive | Indicator for LED power on "H": LED select, "L": Other status |
| LEDOFF | Output | Positive | Indicator for power save mode <br> "H": Power mode select, "L": Other status |
| DDCCLK | Input | Positive | CLK for DDC2B |
| DDCDAT | Input/ <br> Output | Positive | Data for DDC1/2B read/write |
| SEL | Input | Negative | Control function select signal (TTL level) <br> SEL is pulled up in the module. <br> Detail of the functions are mentioned in CONTROL FUNCTIONS. <br> "H" or "open": SEL off, "L": SEL on |
| UP | Input | Negative | Control signal (TTL level) <br> The signal increases the value of the functions selected. <br> UP is pulled up in tha module. <br> "H" or "open": UP off, "L": UP on |
| DOWN | Input | Negative | Control signal (TTL level) <br> The signal decreases the value of the functions selected. DOWN is pulled up in the module. <br> "H" or "open": DOWN off, "L" : DOWN on |
| EXIT | Input | Negative | Control function exit signal (TTL level) EXIT is pulled up in the module. "H" or "open": EXIT off, "L": EXIT on |
| RIGHT Note1 | Input | Negative | Control signal (TTL level) <br> The signal increases the value of the functions selected. <br> RIGHT is pulled up in the module. <br> "H" or "open": RIGHT off, "L": RIGHT on |
| LEFT Note1 | Input | - | Control signal (TTL level) <br> The signal decreases the value of the functions selected. <br> LEFT is pulled up in the module. <br> "H" or "open": LEFT off, "L": LEFT on |
| RESET Note1 | Input | - | Control signal (TTL level) <br> The signal initializes the selected function. <br> RESET is pulled up in the module. <br> "H" or "open": RESET off, "L": RESET on |
| MENUSEL | Input | - | OSD design select signal (TTL level) MENUSEL is pulled up in the module. <br> "H or open": Hierarchical type <br> "L": Toggle type <br> For detail, see OSD MENU SELECT. |


| Symbol | I/O | Logic | Description |
| :--- | :---: | :---: | :--- |
| VDD | - | - | Power supply for Logic and LCD driving $+12 \mathrm{~V}( \pm 5 \%)$ |
| VDDB | - | - | Power supply for backlight $+12 \mathrm{~V}( \pm 5 \%)$ |
| GND | - | - | Ground for system |
| GNDB | - | - | Ground for backlight |

Note 1: This terminal becomes effective only when the value of Button Sel is 7.
Detail of the functions is mentioned in INITIAL VALUE CHANGE FUNCTIONS.
Note 2: Ground for system (GND) and backlight (GNDB) are not connected in the module.
[OSD MENU SELECT]

| OSD menu type | Hierarchical type | Toggle type |
| :---: | :---: | :---: |
| MENUSEL | "H" or "open" | "L" |
| How to adjust | For details, see INITIAL VALUE CHANGE FUCNTION. <br> MENU <br> - Auto Adjust <br> Brightness <br> Clock <br> Position <br> Contrast $\square$ | For details, see INITIAL VALUE CHANGE FUCNTION. |

Note 1: If OSD menu type is changed, the adjustment of the type becomes default value.
Then, It takes 5 seconds to display the changed menu after the supply voltage is input again. And, Indicator for both LEDON and LEDOFF are "ON" at one time during the 5 seconds.
Note 2: The Initial value of Contrast ratio is as follows.
Hierarchical type: 128:1 (typ.)
Toggle type : 100:1 (typ.)
Note 3: On Screen Display
Regarding the use of OSD, please note that there is possibility of conflicts with a patent in Europe and the U.S. Thus, if such conflict might happen when you use OSD, we shall not be responsible for any trouble.

## [Equivalent circuit]

| Symbol | I/O |  | Equivalent circuit |
| :--- | :---: | :--- | :--- |
| LEDON | Output | RN2306 (Toshiba) <br> Or equivalent |  |
| LEDOFF |  |  |  |

<Example of LED circuit>


## INPUT SIGNALS

## INPUT SYNCHRONOUS SIGNAL

This module is corresponding to the synchronous signals below.

| Auto recognition mode |  | Synchronous signal |  |  |
| :--- | :---: | :---: | :---: | :---: |
|  |  | Vsync | Sync. On Green |  |
| Separate synchronous signal mode (HS, Vsync) | Input | Input | Input or no input |  |
| Composite synchronous mode | Input (cs) | No input | Input or no input |  |
| Sync. On Green mode | No input | No input | Input |  |
| Power saving mode | Input (HS) | No input | Input or No input |  |
|  | No input | Input | Input or no input |  |
|  | No Input | No input | No input |  |

Note 1: Power saving mode corresponds to VESA DPMA.
Note 2: The module recognizes Sync on Green signal automatically, when the FUNC in SOG Sel of Sel Data is " $Y$ ". Refer to INITIAL VALUE CHANGE FUNCTION.
In case of Sync on Green mode, input signals (R, G, B, Sync on Green) must be inputted before power on.

## PRESET TIMING

The 14 types of display timing shown below are preset in this module. The input synchronous signals are automatically recognized.

| Preset <br> No. | Display size | Vsync (Hz) | Hsync (kHz) | $\begin{gathered} \text { SYSTEM } \\ \text { CLK } \\ (\mathrm{MHz}) \end{gathered}$ | V Pulse (H) | V B.Porch (H) | H <br> Pulse (CLK) | H B.Porch (CLK) | Sync <br> Logic <br> V, H | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | $640 \times 400$ | 56.432 | 24.830 | 21.053 | 8 | 25 | 96 | 48 | -,- | * |
| 2 | $640 \times 480$ | 59.992 | 31.469 | 25.175 | 2 | 33 | 96 | 48 | -,- | VGA |
| 3 | $720 \times 400$ | 70.087 | 31.469 | 28.322 | 2 | 35 | 108 | 45 | +,- | VGA TXT |
| 4 | $800 \times 600$ | 60.317 | 37.879 | 40.000 | 4 | 23 | 128 | 88 | +,+ | VESA |
| 5 | $640 \times 480$ | 66.667 | 35.000 | 30.240 | 3 | 39 | 64 | 96 | SonG | Macintosh |
| 6 | $640 \times 480$ | 75.000 | 37.500 | 31.500 | 3 | 16 | 64 | 120 | -,- | VESA |
| 7 | $720 \times 400$ | 85.039 | 37.927 | 35.500 | 3 | 42 | 36 | 144 | +,- | VESA |
| 8 | $640 \times 480$ | 85.008 | 43.269 | 36.000 | 3 | 25 | 48 | 112 | -,- | * |
| 9 | $1024 \times 768$ | 60.004 | 48.363 | 65.000 | 6 | 29 | 136 | 160 | -,- | VESA |
| 10 | $800 \times 600$ | 75.000 | 46.875 | 49.500 | 3 | 21 | 80 | 160 | +,+ | VESA |
| 11 | $832 \times 624$ | 74.565 | 49.735 | 57.283 | 3 | 39 | 64 | 224 | SonG | Macintosh |
| 12 | $800 \times 600$ | 85.061 | 53.674 | 56.250 | 3 | 27 | 64 | 152 | +,+ | * |
| 13 | $1024 \times 768$ | 70.069 | 56.476 | 75.000 | 6 | 29 | 136 | 144 | +,+ | VESA |
| 14 | $1024 \times 768$ | 75.029 | 60.023 | 78.750 | 3 | 28 | 96 | 176 | +,+ | VESA |

* Out of specification.

Note 1. Even if the preset timing is entered, a little adjustment of the functions such as Horizontal period, CLKdelay and display position, are required. The adjusted values are memorized in every preset No.
Note 2. This module recognizes the synchronous signals with near preset timing of the frequency of HS, Vsync, even in the case that the signals other than the preset timing that were entered. For instance, it is displayed with presetting number 6 in the case of $640 \times 480$ dot, HS: 37.861 kHz , Vsync: 72.809 Hz an example). Please note that using out of the preset timings may cause wrong magnification ratio, unfitness of control functions or other improper display.
Note 3. The display color scale may be different between Sync on Green Input and others.

## CONTROL FUNCTIONS

## EXPANSION FUNCTION

Expansion mode is a function to expand the screen size in different resolutions. For example, VGA signal has $640 \times 480$ pixels. But, if the display data can be expanded to 1.6 times vertically and horizontally, VGA screen image can be displayed fully on the screen of XGA resolution.

This module automatically recognizes the timing shown in "PRESET TIMING" as an expansion mode.

Please adopt this mode after evaluating display quality because the appearance in expansion mode may become bad in some cases.

The followings show display magnifications for each mode.

| Input display | Magnification |  |  |
| :---: | :---: | :---: | :---: |
|  |  | Vertical | Horizontal |
| XGA | $1024 \times 768$ | 1.0 | 1.0 |
| SVGA | $800 \times 600$ | 1.25 | 1.25 |
| VGA | $640 \times 480$ | 1.6 | 1.6 |
| VGA text | $720 \times 400$ | $1.92 / 1.6$ |  |
| hiearchical $/$ toggle | 1.42 |  |  |
| MAC | $832 \times 624$ | 1.2 | 1.2 |

## <DISPLAY IMAGE>

1) SVGA mode $(800 \times 600)$

2) VGA mode $(640 \times 480)$


Horizontal: $\times 1.6$ (1024 pixels)
Vertical: $\times 1.6$ (768 pixels)
3) VGA text mode $(720 \times 400)$
(1) Hierarchical type

(2) Toggle type

4) $832 \times 624$ MAC mode $(832 \times 624)$


## DDC FUNCTION

This function is corresponding to VESA DDC ${ }^{\text {TM }}$ and EDID ${ }^{\text {TM }}$ (Structure Version 1).
Follow the specifications of the IC when you write data. Then VCLK should be fixed on "H."
Please write a data into necessary addresses in advance when you use this function. Data " 55 H " in address " 00 H " and " FFH " in other address are already programmed when shipping. The input equivalent circuit diagram is as follow.

EDID: Extended Display Identification Data

## <Internal circuit diagram>



DPMS

This function is corresponding to VESA DPMS ${ }^{\text {TM }}$ Standard.

| VESA DPMS Standard |  |  |  |  |  | NL10276AC28-02L |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| State | Signal |  |  | Power saving | Recovery time | Power saving | Recovery time |
|  | Horizontal | Vertical | Video |  |  |  |  |
| On | Pulses | Pulses | Active | None | Not applicable | None | Not applicable |
| Stand-by | No pulses | Pulses | Blanked | Minimum | Short | Maximum | Short |
| Suspend | Pulses | No pulses | Blanked | Substantial | Longer | Maximum | Short |
| Off | No pulses | No pulses | Blanked | Maximum | System dependent | Maximum | Short |

CONTROL FUNCTION ITEMS

| No. | Form |  | Function items |
| :---: | :--- | :--- | :--- |
| 1 | Auto adjust |  | Clock and Position auto adjust |
| 2 | Brightness | H. Size | Horizontal display period adjust |
| 3 | Clock | Clock Phase | CLK-phase adjust |
| 4 | Position | H. Position | Horizontal position adjust |
|  |  | V. Position | Vertical position adjust |
| 5 | Contrast | W, R, G, B | White-level of video signals synchronous color and each color control |
| 6 | Color Level | W W, R, G, B | White-level of video signal synchronous color control |
|  | W | Color-level of video signals synchronous color and each color control |  |
| 7 | Information | OSD Position | Color-level of video signal synchronous color control |
| 9 | Config | Video signal information <br> Display multi-scan function, Hsync and Vsync frequency |  |
| 10 | All Reset | OSD position adjust <br> The display position of OSD can be moved. |  |

In order to achieve the best picture quality, the above functions should be adjusted by setting SEL, UP, DOWN, LEFT, RIGHT, RESET and EXIT signals. Each adjusted value is memorized as soon as SEL is selected or time out mode is valid and the memorized values are not affected even if the power is switched off. But the selected value is not memorized in case that a selected mode is changed to another one before time out or power is turned off before time out.

## FLOW CHART OF CONTROL FUNCTIONS

(1) Hierarchical type (4 switches used for adjustment; SEL, UP, DOWN and EXIT)

* For details, see INITIAL VALUE CHANGE FUNCTIONS - 6. Sel Data, Button Sel -



[^0]

[^1]
(2) Hierarchical type (7 switches used for adjustment; SEL, UP, DOWN, EXIT, LEFT, RIGHT and RESET)

* For details, see INITIAL VALUE CHANGE FUNCTIONS - 6. Sel Data, Button Sel -


Continue to next page


Continue to next page


[^2]

Note 1: The value of the selected signals by LEFT, RIGHT, UP and DOWN key is continuously increased if the input signal is held for more than approx. one second. If it's less than one second, the value is increased by one.
Note 2: RESET signal initializes the value selected by SEL key. "All reset" function initializes all the values adjusted before.
Note 3: No key input for more than ten seconds shall be regarded as "Time out".
Note 4: Contrast $W$ is synchronous with Contrast R, Contrast G and Contrast B.
Note 5: Color Level W is synchronous with Color Level R, Color Level G and Color Level B.

## (3) Toggle type (SEL, UP, DOWN and EXIT)

(UP), (DOWN): + or -
(EXIT): Initialize


Note 1: The value of the selected signals by UP and DOWN key is continuously incremented if the input signal is held for more than approx. one second.
Note 2: RESET signals initializes the value selected by SEL key. All reset function initializes all the values adjusted before.
Note 3: No key input for more than five seconds shall be regarded as "Time out".
Note 4: EXIT is valid when connected to GND for more than approx. two seconds.

## INITIAL VALUE CHANGE FUNCTIONS

Initial value, OSD items, etc. can be changed.
The changed initial value becomes initial value in each preset timing, and is applicable when it is reset (including All Reset).

- How to transpose to initial value change mode.

Hierarchical type : Connect SET and UP to GND at the same time when OSD function is in "information" mode.
Toggle type : Connect SEL to GND while SEL is connected to GND when OSD function is in "All Reset" mode.

- Operation

SEL, UP, DOWN, and EXIT

- How to return to normal mode

Connect EXIT to GND or turn off Power supply when OSD is closed.
Both LEDON and LEDOFF become " H " when this mode is operating.

| No. | Form |  | Function items |
| :---: | :---: | :---: | :---: |
| 1 | Compatible |  | It becomes valid when Toggle menu type is selected only. <br> YES: Sync on Green <br> The initial value of contrast (RGB synchronous) becomes 128 . <br> NO : No Sync on Green <br> The initial value of contrast (RGB synchronous) becomes 100 . The NEC factory value: NO. |
| 2 | Brightness |  | The maximum value of brightness of backlight can be changed. Initial value: 100 |
| 3 | Color Level |  | The initial value of color level can be changed. Initial value: 128 |
| 4 | Contrast |  | The initial value of contrast can be changed. <br> Initial value: 128 <br> (When NO is selected by Compatible in Toggle menu type, the initial value of RGB synchronous only is 100 . <br> As for RGB synchronous, this clause setup value becomes valid as the Initial value regardless of the selections state of Compatible.) |
| 5 | LCD Data | Mode | No. of PRESET TIMINGS is displayed. |
|  |  | HSE | The initial value of the double value can be changed. |
|  |  | Delay | The value of the CLK delay can be changed. (Initial value can not be changed.) <br> Initial value: 0 |
|  |  | VD | The initial value of the up-down position of the display position can be changed. |
|  |  | HD | The initial value of the left-right position of the display position can be changed. |
|  |  | Auto Adjust | Auto adjust |
|  |  | Par. Delay | The compensation value of Delay of the auto adjust can be changed. Valid range: 0 to 255 |
|  |  | Par. HD | The compensation value of horizontal position of the auto adjust can be changed. <br> Valid range: 0 to 127 |

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| No. | Form |  | Function items |
| :---: | :---: | :---: | :---: |
| 5 | LCD Data | LCD Reset | The values in No. 5 LCD Data are returned to the NEC factory values. |
| 6 | Sel Data | S - Lvl Sel | The adjustment that RGB is independent can be made impossible by the adjustment of the Color level. In this case, the adjustment item that RGB is independent is not displayed on the menu screen. <br> ON: Independent adjustment is available. <br> OFF: Independent adjustment is impossible Initial value: ON. |
|  |  | $\mathrm{S}-\mathrm{Cnt} \mathrm{Sel}$ | The adjustment that RGB is independent can be made impossible by the adjustment of the Color level. In this case, the adjustment item that RGB is independent is not displayed on the menu screen. <br> ON : Independent adjustment is available. <br> OFF: Independent adjustment is impossible Initial value: ON. |
|  |  | SOG Sel | Select or non-select of Sync on Green and display or non-display of select menu are selected. When non-display selected, Config is not displayed. <br> - FUNC (Y: For Sync on Green select) <br> ( $\mathrm{N}:$ For Sync on Green non-select) <br> - MENU (Y: Select menu display.) <br> ( N : Select menu non-display.) <br> Initial value: $\mathrm{Y} / \mathrm{N}$ <br> * It becomes effective only when the Hierarchical menu type is selected. But it becomes invalid when Toggle menu type is selected. |
|  |  | MENU Sel | B (blue) or S (transparency) can be selected for the Background Color of the OSD menu. <br> Initial value: B |
|  |  | Button Sel | 4 (SEL, UP, DOWN and EXIT) or 7 (SEL, UP, DPWN, RIGHT, LEFT, EXIT and RESET) number of adjust switch are selected. <br> Initial value: 7 |
|  |  | PSOSD Sel | Display or no display for Power Save OSD can be selected. (For detail, see INFORMARION DISPLAY BY OSD) <br> ON: Display OFF: No display Initial value: OFF |
| 7 | Information |  | Input signal information is displayed. |
| 8 | OSD Position |  | The initial value of OSD display position can be changed. <br> C: Center UL: Upper left UR: Upper right DL: Down left DR: Down right Initial value: C |
| 9 | Used Time |  | Use time in normal and Power save state is displayed. <br> Use time is recorded in every five minutes. But it is not recorded when Power supply is cut off within five minutes. |
| 10 | Full Reset |  | All value is returned to NEC factory value. It takes about seven seconds. |

* The value of No. 5 LCD Data are memorized in every Preset timing.
* Selected adjust item is displayed with OSD.
* Data except Auto Adjust of 5. LCD Data are saved at the moment when it was returned in the extreme upper menu. But it is not kept when the power supply is cut off halfway.
* All adjust value is returned to the NEC factory value when menu type is changed.
* Evaluate compensation value of Par. Delay and Par. HD of No. 5 LCD Data, and be decided.


## INFORMARION DISPLAY BY OSD

1. Power save : Frequency of horizontal, vertical synchronous signal is displayed for four seconds at the time of the Power Save mode switchover.

Power Save
Hf 0.0 kHz
Vf 0.0 kHz

* It is displayed when Horizontal synchronous signal frequency (Hf) and vertical synchronous signal frequency (Vf) are the following.
(1) $\mathrm{Hf}<10 \mathrm{KHz}$
(2) $\mathrm{Vf}<40 \mathrm{~Hz}$
(3) (1) and (2)
* It is not displayed when "Power Save OSD no display" form is selected with INITIAL VALUE CHANGE

FUNCTIONS - 6. Sel Data, PSOSD Sel -
2. Out of Range : Each frequency is displayed for four seconds when the Horizontal and Vertical synchronous signal outside the corresponding range is inputted.

## Out of Range <br> Hf 64.0 kHz <br> Vf 60.0 kHz

* It is displayed when Horizontal synchronous signal frequency (Hf) and vertical synchronous signal frequency (Vf) are the following
(1) $10 \mathrm{KHz} \leq \mathrm{Hf}<23 \mathrm{KHz}$ or $61.5 \mathrm{KHz}<\mathrm{Hf}$
(2) $40 \mathrm{~Hz} \leq \mathrm{Vf}<50 \mathrm{~Hz}$ or $200 \mathrm{~Hz}<\mathrm{Vf}$
(3) (1) and (2)

But it is not displayed when either Hf or Vf is power save mode.

* Display or no display for Out of Range can not be selected.

3. Disable : It is displayed for four seconds that the switch for adjust is disable.


INPUT SIGNAL AND DISPLAY POSITION - XGA STANDARD TIMING -

Pixels

| $\mathrm{D}(0,0)$ | $\mathrm{D}(0,1)$ | $\mathrm{D}(0,2)$ | $\cdots$ | $\cdots$ | $\mathrm{D}(0,1023)$ |
| :---: | :---: | :---: | :--- | :---: | :---: |
| $\mathrm{D}(1,0)$ | $\mathrm{D}(1,1)$ | $\mathrm{D}(1,2)$ | $\cdots$ | $\cdots$ | $\mathrm{D}(1,1023)$ |
| $\mathrm{D}(2,0)$ | $\mathrm{D}(2,1)$ | $\mathrm{D}(2,2)$ | $\cdots$ | $\cdots$ | $\mathrm{D}(2,1023)$ |
| $\cdot$ | $\cdot$ | $\cdot$ |  |  | $\cdot$ |
| $\cdot$ | $\cdot$ | $\cdot$ |  | $\cdot$ |  |
| $\cdot$ | $\cdot$ | $\cdot$ |  | $\cdot$ |  |
| $\mathrm{D}(767,0)$ | $\mathrm{D}(767,1)$ | $\mathrm{D}(767,2)$ | $\cdots$ | $\cdots$ | $\mathrm{D}(767,1023)$ |



Note The tda should be more than 4ns

OPTICAL CHARACTERISTICS

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit | Remark |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Contrast ratio | CR | $\theta \mathrm{R}=0^{\circ}, \theta \mathrm{L}=0^{\circ}, \theta \mathrm{D}=0^{\circ},$ <br> White/Black, at center | 80 | 150 | - | - | Note 2 |
| Luminance | Lvmax | White, at center | 150 | 200 | - | $\mathrm{cd} / \mathrm{m}^{2}$ | Note 3 |
| Luminance uniformity | - | White | - | - | 1.30 | - | Note 4 |

Reference data

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit | Remark |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Contrast ratio | CR | Best contrast angle $\theta \mathrm{R}=0^{\circ}, \theta \mathrm{L}=0^{\circ}, \theta \mathrm{D}=5^{\circ}$ White/Black, at center | - | 300 | - |  | Note 2 |
| Color gamut | C | $\theta \mathrm{R}=0^{\circ}, \theta \mathrm{L}=0^{\circ}, \theta \mathrm{U}=0^{\circ}, \theta \mathrm{D}=0^{\circ}$ <br> at center, to NTSC | 35 | 40 | - | \% | - |
| Viewing angle range | $\theta \mathrm{R}$ | $\mathrm{CR}>10, \theta \mathrm{U}=0^{\circ}, \theta \mathrm{D}=0^{\circ}$ <br> White/Black, at center | 70 | 85 | - | deg. | Note 4 |
|  | $\theta L$ |  | 70 | 85 | - | deg. |  |
|  | $\theta \mathrm{U}$ | $\mathrm{CR}>10, \theta \mathrm{R}=0^{\circ}, \theta \mathrm{L}=0^{\circ}$ <br> White/Black, at center | 70 | 85 | - | deg. |  |
|  | $\theta \mathrm{D}$ |  | 70 | 40 | - | deg. |  |
| Response time | Ton | Black to White | - | 34 | 80 | ms | Note 6 |
|  | Toff | White to Black | - | 40 | 80 |  |  |

Note 1. The luminance is measured after 20 minutes from the module works, with all pixels in "white."
The typical value is measured after luminance saturation.
Display mode: VESA XGA-75 Hz
RGB input voltage: $0.7 \mathrm{Vp}-\mathrm{p}$
Contrast: Default

Note 2. The contrast ratio is calculated by using the following formula.
Contrast ratio $(\mathrm{CR})=\frac{\text { Luminance with all pixels in "white" }}{\text { Luminance with all pixels in "black" }}$

Note 3. The luminance is measured after 20 minutes from the module works, with all pixels in "white". The typical value is measured after luminance saturation, more than one hour after burn-in. The timing is XGA standard mode, preset No. 9.


Note 4. Luminance uniformity is calculated by using the following formula.
Luminance uniformity $=\frac{\text { Maximum luminance }}{\text { Minimum luminance }}$

The luminance is measured at near the five points shown below.


Note 5. Definitions of viewing angle are as follows.


Note 6. Definitions of response time is as follows.
Photo-detector output signal is measured when the luminance changes "black" to "white" or "white" to "black".


RELIABILITY TEST

| Test item | Test condition | Judgment |
| :---: | :---: | :---: |
| High temperature/humidity operation | $50 \pm 2^{\circ} \mathrm{C}, 85 \%$ relative humidity 240 hours, Display data is black. | Note 1 |
| Heat cycle (operation) | $<1>0^{\circ} \mathrm{C} \pm 3^{\circ} \mathrm{C} \ldots 1$ hour $55^{\circ} \mathrm{C} \pm 3^{\circ} \mathrm{C} \cdots 1$ hour <br> <2> 50 cycles, 4 hours/cycle <br> $<3>$ Display data is black. | Note 1 |
| Thermal shock (non-operation) | ```<1> -20}\mp@subsup{}{}{\circ}\textrm{C}\pm\mp@subsup{3}{}{\circ}\textrm{C}\cdots30\mathrm{ minutes 60}\mp@subsup{}{}{\circ}\textrm{C}\pm\mp@subsup{3}{}{\circ}\textrm{C}\cdots30\mathrm{ minutes <2> 100 cycles <3> Temperature transition time is within 5 minutes.``` | Note 1 |
| Vibration (non-operation) | $<1>5-100 \mathrm{~Hz}, 19.6 \mathrm{~m} / \mathrm{s}^{2}(2 \mathrm{G})$ <br> 1 minute/cycle, <br> $\mathrm{X}, \mathrm{Y}, \mathrm{Z}$ direction <br> <2> 50 times each direction | Notes 1, 2 |
| Mechanical shock (non-operation) | <1> $294 \mathrm{~m} / \mathrm{s}^{2}(30 \mathrm{G}), 11 \mathrm{~ms}$ <br> $X, Y, Z$ direction <br> <2> 3 times each direction | Notes 1, 2 |
| ESD (operation) | $150 \mathrm{pF}, 150 \Omega, \pm 10 \mathrm{KV}$ <br> 9 places on a panel <br> Note 3 <br> 10 times each place at one-second intervals | Note 1 |
| Dust (operation) | 15 kinds of dust (JIS-Z 8901) <br> Hourly 15 seconds stir, 8 times repeat | Note 1 |

Notes 1. Display function is checked by the same condition as LCD module out-going inspection.
2. Physical damage.
3. Discharge points are shown in the figure.


## GENERAL CAUTIONS

Because next figures and sentences are very important, please understand these contents as follows.

| ! CAUTION | This figure is $a$ mark that you will get hurt and/or the module will have damages <br> when you make a mistake to operate. |
| :--- | :--- |


| 4 | This figure is a mark that you will get an electric shock when you make a mistake to operate. |
| :---: | :--- |
|  | This figure is a mark that you will get hurt when you make a mistake to operate. |

## ! caution

Do not touch an inverter -- on which is stuck a caution label -- while the LCD module is under the operation, because of dangerous high voltage.
(1) Caution when taking out the module a Pick the pouch only, in taking out module from a carrier box.
(2) Cautions for handling the module
a) As the electrostatic discharges may break the LCD module, handle the LCD module with care against electrostaic discharges.
b) As the LCD panel and backlight element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
c) As the surface of polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
d) Do not pull the interface connectors in or out while the LCD module is operating.
e) Put the module display side down on a flat horizontal plane.
f) Handle connectors and cables with care.
g) When the module is operating, do not lose CLK, HS or Vsync signal. If any one of these signals is lost, the LCD panel would be damaged.
h) The torque to mounting screw should never exceed $0.392 \mathrm{~N} \cdot \mathrm{~m}(4 \mathrm{kgf} \cdot \mathrm{cm})$.
(3) Cautions for the atmosphere
a) Dew drop atmosphere should be avoided.
b) Do not store and/or operate the LCD module in a high temperature and/or high humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
c) This module uses cold cathod fluorescent lamps. Therefore, the life of lamps becomes short if the module is operated under the low temperature environment.
d) Do not operate the LCD module in a high magnetic field.
(4) Caution for the module characteristics
a) Do not apply the fixed pattern for a long time to the LCD module. Applying fixed pattern for a long time may cause image sticking.
(5) Other cautions
a) Do not disassemble and/or reassemble LCD module.
b) Do not readjust any variable resistors or switches in the module.
c) When returning the module for repair or etc., please pack the module properly to avoid any damages. We recommend using the original shipping packages.

Liquid Crystal Display has the following specific characteristics. There are not defects nor malfunctions.

The display condition of LCD module may be affected by the ambient temperature.
The LCD module has cold cathode tube for backlight. Optical characteristics, like luminance or uniformity, will change during time.

Uneven brightness and/or small spots may be noticed depending on different display patterns.

## OUTLINE DRAWINGS

## Front View (Unit in mm)



Rear View (Unit in mm)
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